

### **REMARKS**

By this amendment, claims 1, 3- 4, 11, and 12 have been amended. Accordingly, claims 1-4, 11, and 12 are currently pending in the application, of which claims 1, 3, and 11 are independent claims.

Applicants respectfully submit that the above amendments do not add new matter to the application and are fully supported by the specification. Support for the amendments may be found at least in Fig. 3A and Fig. 3B.

In view of the above amendments and the following Remarks, Applicants respectfully request reconsideration and timely withdrawal of the pending rejections for the reasons discussed below.

#### ***Status of Amendments***

An After-Final Reply was filed on December 22, 2006 with amendments to claims 1, 3, 4, 11, and 12. The examiner indicated in the Advisory Action mailed January 26, 2007 that those amendments would not be entered. Please DO NOT enter those amendments. Please enter only the amendments included in this Reply, and please examine the claims based upon these amendments.

#### ***Rejections Under 35 U.S.C. § 102***

Claims 1-3 and 11 stand rejected under 35 U.S.C. § 102(b) as being allegedly anticipated by U. S. Patent No. 6,506,635, issued to Yamazaki, *et al.* ("Yamazaki, *et al.*").

In order for a rejection under 35 U.S.C. § 102(b) to be proper, a single reference must disclose every claimed feature. To be patentable, a claim need only recite a single novel feature that is not disclosed in the cited reference. Thus, the failure of a cited reference to disclose one or more claimed features renders the 35 U.S.C. § 102(b) rejection improper.

Applicants submit that Yamazaki, *et al.* fail to disclose every feature of the present invention. Specifically, Yamazaki, *et al.* fail to disclose every feature of independent claims 1, 3, and 11 as amended. Claim 1 as amended recites, *inter alia*:

wherein the plurality of channel regions comprises a first channel region and a second channel region, the thin film transistor in the pixel array portion further comprises a first source/drain region, a second source/drain region, and an offset region, and the offset region directly contacts the first channel region and the second channel region. (emphasis added)

Yamazaki, *et al.* fail to disclose at least these features. Accordingly, Yamazaki, *et al.* fail to disclose every feature of claim 1. Claims 3 and 11 are amended to include similar limitations. Thus, for at least these reasons, Yamazaki, *et al.* also fail to disclose every feature of claims 3 and 11.

Claims 1-4 and 11-12 stand rejected under 35 U.S.C. § 102(b) as being allegedly anticipated by U. S. Patent Application Publication No. 2003/0062499 filed by Yamazaki ("Yamazaki").

Applicants submit that Yamazaki fails to disclose every feature of the present invention. Specifically, Yamazaki fails to disclose every feature of independent claims 1, 3, and 11 as amended. Claim 1 as amended recites, *inter alia*:

wherein the plurality of channel regions comprises a first channel region and a second channel region, the thin film transistor in the pixel array portion further comprises a first source/drain region, a second source/drain region, and an offset region, and the offset region directly contacts the first channel region and the second channel region. (emphasis added)

Yamazaki fails to disclose at least these features. Accordingly, Yamazaki fails to disclose every feature of claim 1. Claims 3 and 11 are amended to include similar limitations. Thus, for at least these reasons, Yamazaki also fails to disclose every feature of claims 3 and 11.

Accordingly, Applicants respectfully request withdrawal of the 35 U.S.C. § 102(b) rejections of claims 1, 3, and 11. Claims 2, 4, and 12 depend from claims 1, 3, and 11, respectively, and are allowable at least for this reason. Since none of the other prior art of record discloses or suggests all the features of the claimed invention, Applicants respectfully submit that independent claims 1, 3, and 11, and all the claims that depend therefrom, are allowable.

**CONCLUSION**

Applicants believe that a full and complete response has been made to the pending Office Action and respectfully submit that all of the stated grounds for rejection have been overcome or rendered moot. Accordingly, Applicants respectfully submit that all pending claims are allowable and that the application is in condition for allowance.

Should the Examiner feel that there are any issues outstanding after consideration of this response, the Examiner is invited to contact the Applicants' undersigned representative at the number below to expedite prosecution.

Prompt and favorable consideration of this Reply is respectfully requested.

Respectfully submitted,

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